

Notice of References Cited	Application/Control No. 10/038,689		Applicant(s)/Patent Under Reexamination RICH ET AL.	
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	IEEE Std 1076.4-1995 - IEEE Standard for VITAL Application-Specific Integrated Circuit (ASIC) Modeling Specification - 1995 (relevant pages)			
	V	IEEE Std 1481-1999 - IEEE Standard for Integrated Circuit (IC) Delay and Power Calculation System - 1999 (relevant pages)			
	W	Standard Delay Format Specification - Version 2.1 - 1994 (relevant pages)			
	X	EE Times Article - 1996 - Use of equations in DCL.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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	U	"Standardizing ASIC Libraries in VHDL Using VITAL: a Tutorial"; Krolikoski S.J.; IEEE 1995 Custom Integrate Circuits Conference.
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